AIPUPUWER®

DC-DC Converter NNV25-05S05B3N Series



Typical Features

- ◆ Fixed input voltage, Isolated & unregulated output, Output power 0.25W
- ♦ High Efficiency up to 82%
- Small compact SIP packing
- ◆ Isolation Voltage 3000VDC
- ♦ Operating Temperature: -40°C~+85°C
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25 $^{\circ}$ C

Application Field

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Medel	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitiv e Load	Ripple & Noise (Max.)		iency %)
Model	Nominal	Range	Voltage (VDC)	Current(mA) MAX./Min.	Full load Typ.	No Load Typ.	uF	mVp-p	Min.	Тур.
NNV25-05S05B3N	5	4.5 - 5.5	5	50	56	6	2200	100	80	82

Note:

1. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power.

Input Specifications								
Item	Test Condition	Min.		Тур.	Max.	Unit		
Input Overshoot Voltage (1Second.max.)	5Vdc Input -0.		-		9	VDC		
Input Filter	Capacitor Filter							
Output Specifications								
ITEM	Working Conditions		Min.	Тур.	Max.	Unit		
Output Power			0.02		0.25	W		
Output Voltage Accuracy	Nominal input, Full lo	ad		±2	±5	%		
Load Regulation	10% ~ 100% nominal load				15			
Line Voltage Regulation	Input Voltage Change±1%				±1.2			

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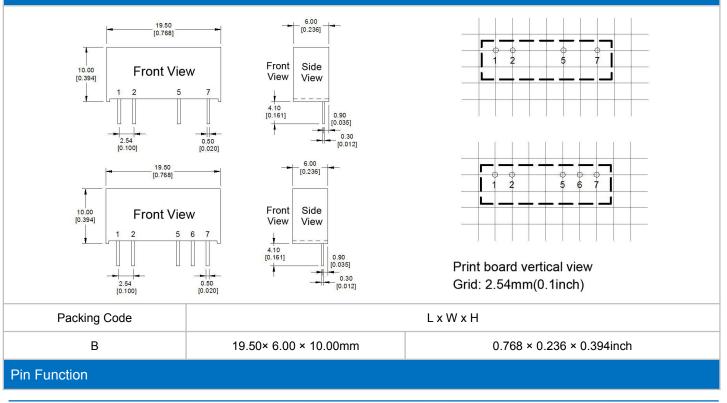
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Ripple & Noise①	Nominal input, full load, 20MHZ bandwidth		75	100	mVp-p		
Temperature Drift Coefficient	100% Full Load			±0.03	%/°C		
Output Short Circuit Protection Continuous short-o			-circuit protection, self-recovery				
NOTE:							
①Ripple & Noise tested by twisted	I-pair method,						
General Specifications							
Switching Frequency	typical		370КНz (Тур.)				
Operating Temperature	Refer to Temperature Derating Curv	e	-40°C ~ +85°C				
Storage Temperature			-55℃ ~ +125℃				
Shell temperature rise during work	Within Temperature Derating Curve	•	25 ℃				
Relative Humidity	No condensing		5%~95%				
Case Material		Black fl	Black flame-retardant heat-resistant Plastic(UL94 V-0)				
Pin Withstand Welding Temp	Distance to case 1.5mm, 10S		300℃ MAX				
Isolation Voltage	Test 1 minute, leakage current< 0.5mA		3000Vdc				
Isolation Capacitor	Input/output, 100KHz/0.1V		20 pF (Typ.)				
MTBF MIL-HDBK-217F@25°C			35X10 ⁵ Hrs				
Product Weight			2	1g(Typ.)			
Deckers	Tube(525*18*10mm) Inner		25PCS				
Package	Carton(542*110*155mm)	2000PCS(Total 80Tubes)			es)		

Packing Dimension



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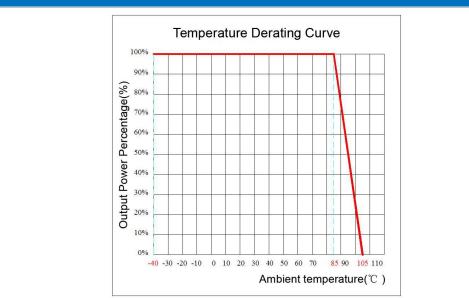
DPO(Probe exclude

p and earth wire)

Pin Function	1	2	3	4	5	6	7
Single(S)	+Vin	GND	NP	NP	-Vo	NP	+Vo
Note: if the definition of pin	is not in accord	dance with the i	model selectior	n manual, pleas	e refer to the la	bel on actual ite	em.
Ripple& Noise Test: (Tw	visted Pair Me	thod 20MH	Z bandwidth))			
Test Method:							
a.12# twisted pair to connect	ct, Oscilloscope	bandwidth set	as 20MHz,		L,	Power Line	
100M bandwidth probe, terr	ninated with 0.1	uF polypropyle	ene capacitor	31L	g Plate		
and 10uF high frequency low resistance electrolytic capacitor in							
parallel, oscilloscope set as	Sample patterr	1.		Input	addie www.		

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.

Temperature Curve



Design and Application Circuit Recommended

1. Output load requirements

a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.

b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

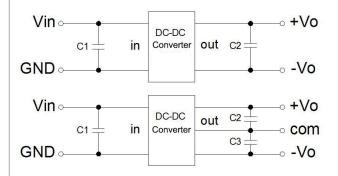
Sample twisted pair 30±2cm

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2. Recommended circuit

In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1. (But for the actual output power of application circuit is less than 0.5W, suggest not to connect external capacitor)

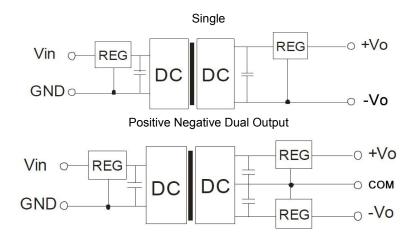


Vin (Vdc)	C1 (µF)	Vout (Vdc)	C2 (µF)	Vout (Vdc)	C2,C3 (µF)
3.3/5	4.7	3.3/5	10	±3.3/±5	4.7
12	2.2	9	4.7	±9	2.2
<mark>15</mark>	1	12	2.2	±12	1
24	1	15	1	±15	0.47
		24	0.47	±24	0.22

Recommended capacitive load value(Table 1)

3. Output regulated voltage and over voltage protection circuit

The simplest device to protect output regulated voltage, over voltage and over current is to cascade a linear regulator with overheat protection at input or output terminal, and connect a capacitor filter net(see below picture), filter capacitive value recommended see table 1, Linear regulator is chosen according to the actual voltage, current needed in working, or choose our NW series products.



Note:

1. This product cannot be used in parallel, and do not support hot-plugging;

2.If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual;

- 3. All index testing methods in this datasheet are based on our Company's corporate standards.
- 4. The product specification may be changed at any time without prior notice.

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