Typical Features

- ◆ Fixed input voltage, Isolated & regulated output, 1W
- ◆ Transfer efficiency up to 74%
- ◆ Compact SIP package
- ◆ No external component required
- ◆ Isolation Voltage 4000VDC
- ◆ Operating Temperature: -40℃～+85℃
- ◆ Plastic Case, meet UL94 V-0 standard

Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25℃

Application Field

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Model	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitive Load	Ripple & Noise (Max.)	Efficiency (%)full load, input nominal voltage	
	Nominal	Range	Voltage (VDC)	Current(mA) MAX./Min.	Full load Typ.	No Load Typ.	uF	mVp-p	Min.	Typ.
NW1-05S05H4	5	4.75-5.25	5	200	290	10	220	100	67	69
NW1-12S05H4	12	11.4-12.6	5	200	108	10	220	100	72	74

Note:

1. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power.
2. The positive and negative output two-circuit capacitive load is the same.

Input Specifications

Item	Working Conditions	Min.	Typ.	Max.	Unit
Input Overshoot Voltage (1Second.max.)	5Vdc Input	-0.7	-	9	Vdc
	12Vdc Input	-0.7	-	18	
Input Filter	Capacitor Filter				

Output Specifications

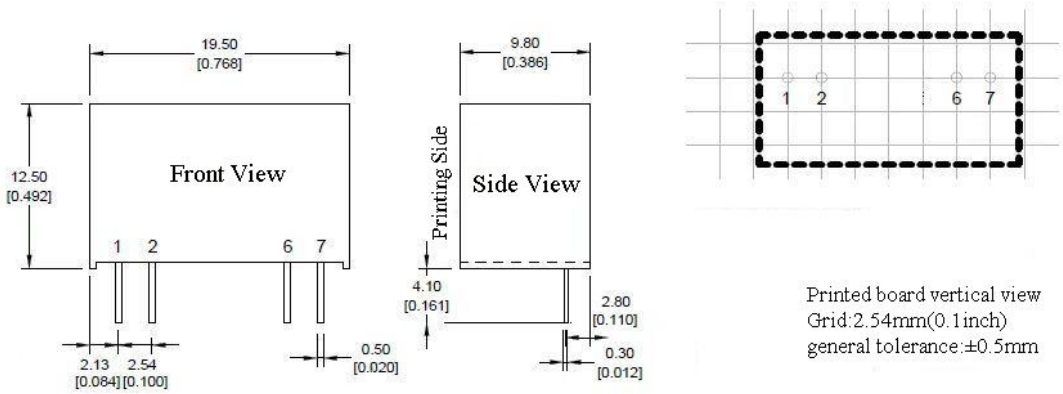
Item	Working Conditions	Min.	Typ.	Max.	Unit
Output Power		0.1	--	1	W
Output Voltage Accuracy	Nominal input, Full load	--	±2	±3	%
Load Regulation	10% ~ 100% load	--	--	±3	
Line Regulation	Input Voltage Change±1%;	--	--	±0.5	
Ripple & Noise①	Nominal input, full load, 20MHZ bandwidth	--	80	100	mVp-p
Temperature Drift Coefficient	100% Full Load	--	--	±0.03	%/°C
Output Short Circuit Protection	Continuous, self-recovery				

NOTE:①Ripple & Noise tested by twisted-pair method.

General Specifications

Switching Frequency(full load)	typical	260KHz (Typ.)
Operating Temperature	Refer to Temperature Derating Curve	-40℃ ~ +85℃
Storage Temperature		-55℃ ~ +125℃
Shell temperature rise during work	Within Temperature Derating Curve	25℃(Typ.)
Relative Humidity	No condensing	5%~95%
Case Material		Black flame-retardant heat-resistant Plastic(UL94 V-0)
Pin Resistance to Soldering Temp	1.5mm away from the shell, 10second	300℃ MAX
Isolation Voltage	Test 1 minute, leakage current＜0.5mA	4000Vdc
Isolation Capacitor	Input/Output, 100KHz/0.1V	10 pF (Typ.)
MTBF	MIL-HDBK-217F@25℃	35X10^{5}Hrs
Product Weight		3.7g (Typ.)
Packing Method	Single Tube(225*20.5*12.5mm)	10PCS
	Single Box(245*155*85mm)	480PCS(Total 48 Tubes)

Packing Dimension



Packing Code	L x W x H

H	19.50× 9.80 × 12.50mm	0.768 × 0.386 × 0.492inch

Pin Function

Pin Function	1	2	3,4,5	6	7
Single(S)	+Vin	GND	NP	-Vo	+Vo

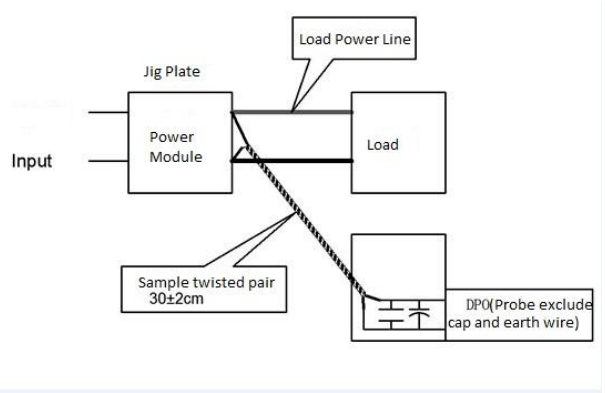
Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

Ripple& Noise Test: (Twisted Pair Method 20MHZ bandwidth)

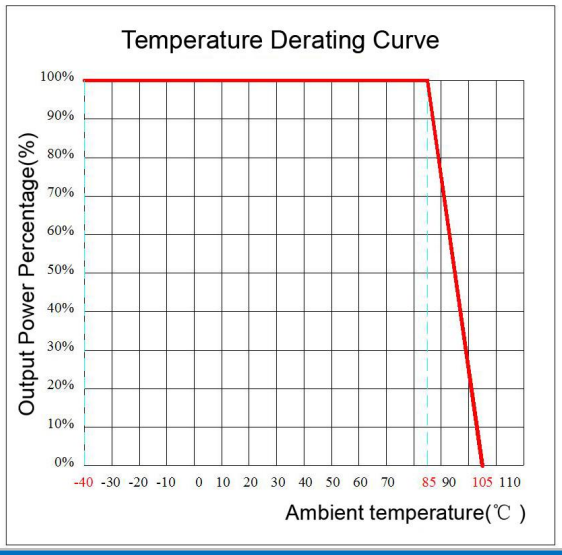
Test Method:

a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



Temperature Curve



Design and Application Circuit Recommended

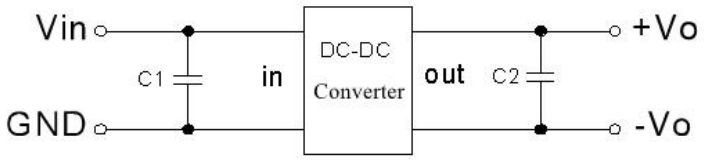
1. Output load requirements

a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.

b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

2. Recommended circuit

In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1. (But for the actual output power of application circuit is less than 0.5W, suggest not to connect external capacitor)



Recommended capacitive load value(Table 1)

Vin (Vdc)	C1 (μF)	Vout (Vdc)	C2 (μF)	Vout (Vdc)	C2,C3 (μF)
3.3/5	4.7	3.3/5	10	±3.3/±5	4.7
12	2.2	9	4.7	±9	2.2
15	1	12	2.2	±12	1
24	1	15	1	±15	0.47
--	--	24	0.47	±24	0.22

Note:

1.This product cannot be used in parallel, and do not support hot-plugging;

2.If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual;

3. All index testing methods in this datasheet are based on our Company's corporate standards

4. The product specification may be changed at any time without prior notice.

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