

Typical Features

- ◆ Fixed input voltage, Isolated & unregulated output, Output power 1W
- ◆ High Efficiency up to 86%
- ◆ Small compact SIP packing
- ◆ Isolation Voltage 1500VDC
- ◆ Operating Temperature: -40℃ ~ +105℃
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25℃

Application Field

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Certificate	Part No.	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitive Load	Ripple & Noise (20MHZ Bandwidth) Max./Typ.	Efficiency (%)@output full load, nominal input voltage	
		Nominal	Range	Voltage (VDC)	Current (mA) MAX./Min.	Full Load Typ.	No Load Typ.	uF	mVp-p	Min.	Typ.
-	FN1-3V3S3V3AN	3.3	2.97 - 3.63	3.3	303/30	370	8	2400	75/40	74	76
-	FN1-3V3S05AN			5	200/20	358	8	2400	75/40	81	83
-	FN1-3V3S12AN			12	83/9	340	10	560	75/40	83	85
-	FN1-3V3S15AN			15	67/7	345	20	560	75/40	81	83
-	FN1-3V3S24AN			24	42/5	360	30	220	100/80	81	83
-	FN1-05S3V3AN	5	4.5 - 5.5	3.3	303/30	250	8	2400	75/40	78	80
UL/ RoHS	FN1-05S05AN			5	200/20	225	8	2400	75/40	83	85
-	FN1-05S09AN			9	111/12	227	10	1000	75/40	83	85
CE	FN1-05S12AN			12	83/9	220	10	560	75/40	83	85
CE	FN1-05S15AN			15	67/7	220	18	560	75/40	83	85
-	FN1-05S24AN			24	42/5	266	18	220	100/80	82	84
-	FN1-09S09AN	9	8.1 - 9.9	9	111/12	128	10	560	75/40	82	84

-	FN1-12S3V3AN	12	10.8 - 13.2	3.3	303/30	98	10	2400	75/40	80	82
UL/ CB/ RoHS	FN1-12S05AN			5	200/20	96	10	2400	75/40	84	86
-	FN1-12S09AN			9	111/12	92	10	1000	75/40	84	86
UL/ CE/ CB	FN1-12S12AN			12	83/9	90	10	560	75/40	84	86
-	FN1-12S15AN			15	67/7	90	10	560	75/40	84	86
-	FN1-12S24AN			24	42/5	92	10	220	100/80	83	85
-	FN1-15S05AN	15	13.5 - 16.5	5	200/20	78	10	2400	75/40	83	85
-	FN1-15S12AN			12	83/9	76	10	1000	75/40	84	86
-	FN1-15S15AN			15	67/7	76	10	560	75/40	83	85
-	FN1-15S24AN			24	42/5	75	10	470	100/80	83	85
-	FN1-24S3V3AN	24	21.6 - 26.4	3.3	303/30	48	8	2400	75/40	80	82
RoHS	FN1-24S05AN			5	200/20	47	8	2400	75/40	82	84
-	FN1-24S09AN			9	111/12	48	8	1000	75/40	83	85
-	FN1-24S12AN			12	83/9	48	8	560	75/40	84	86
-	FN1-24S15AN			15	67/7	48	8	560	75/40	83	85
-	FN1-24S24AN			24	42/5	49	8	220	100/80	83	85

Note: ① The ripple & noise test method adopts the twisted pair method.

Input Specifications

Item	Test Condition	Min.	Typ.	Max.	Unit
Input Overshoot Voltage (1Second.max.)	3.3Vdc Input	-0.7	-	7	VDC
	5Vdc Input	-0.7	-	9	
	9Vdc Input	-0.7	-	12	
	12Vdc Input	-0.7	-	18	
	15Vdc Input	-0.7	-	21	
	24Vdc Input	-0.7	-	30	
Input Filter	Capacitor Filter				
Hot Plug	Unavailable				

Output Specifications

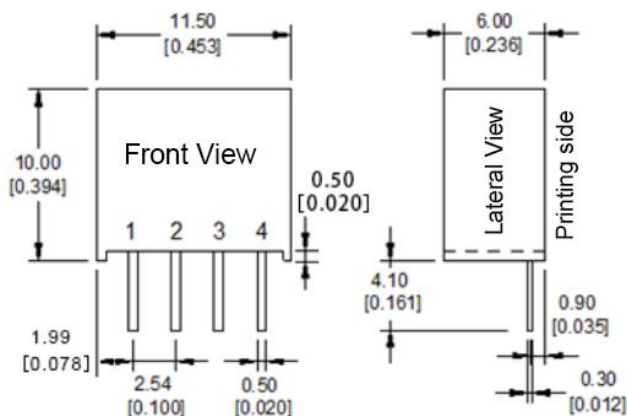
ITEM	Working Conditions		Min.	Typ.	Max.	Unit
Output Power			0.1	--	1	W
Output Voltage Accuracy			See the error envelope curve (Figure 1)			
Load Regulation	10% ~ 100% load	3.3Vdc output	--	15	20	%
		Other output	--	10	15	
Line Voltage Regulation	Input Voltage Change±1%	3.3Vdc output	--	--	1.5	--
		Other output	--	--	1.2	

Temperature Drift Coefficient	100% Full Load	--	--	±0.03	%/°C
Output Short Circuit Protection	Continuous, self-recovery				

General Specifications

ITEM	Working Conditions	Min.	Typ.	Max.	Unit
Switching Frequency	Nominal input voltage, full load	--	260	--	KHz
Operating Temperature	Refer to Temperature Derating Curve (Figure 2)	-40	--	105	°C
Storage Temperature		-55	--	+125	
Shell temperature rise during operation	Ta=25°C	--	30	--	
Pin withstand welding temp	Distance to case 1.5mm, 10s	--	--	300	
Relative Humidity	No condensing	5	--	95	%RH
Isolation Voltage	I/P-O/P, test 1 minute, leakage current<0.5mA	1500	--	--	VDC
Insulation Resistance	I/P-O/P, insulation voltage 500VDC	1000	--	--	M Ω
Isolation Capacitor	Input/Output, 100KHz/0.1V	--	20	--	pF
Vibration		10-150Hz, 5G, 30 Min. along X, Y and Z			
MTBF	MIL-HDBK-217F@25°C	3500	--	--	K hours
Case Material	Black flame retardant and heat resistant plastic (UL94-V0)				
Product Weight	1.4g (Typ.)				
Cooling Method	Natural air cooling				
Package Method	Tube(525*18*10mm)	43PCS			
	Inner Box(542*110*155mm)	3440PCS(Total 80Tubes)			
Dimension	L x W x H	11.50× 6.00 × 10.00mm		0.453 × 0.236 × 0.394inch	

Packing Dimension



Unit:
Grid spacing 2.54*2.54mm
Unit: mm [inch]
Terminal section tolerance: ±0.10 [±0.004]
Unmarked tolerance: ±0.50 [±0.020]

Packing Code	L x W x H	
A	11.50× 6.00 × 10.00mm	0.453 × 0.236 × 0.394inch

Pin Function

Single(S)	1	2	3	4
	GND	+Vin	-Vo	+Vo

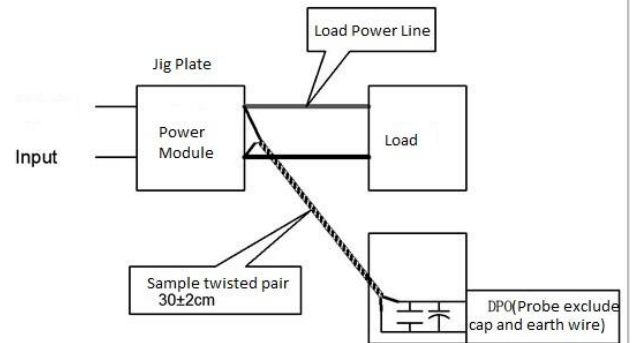
Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

Ripple & Noise Test: (Twisted Pair Method 20MHZ bandwidth)

Test Method:

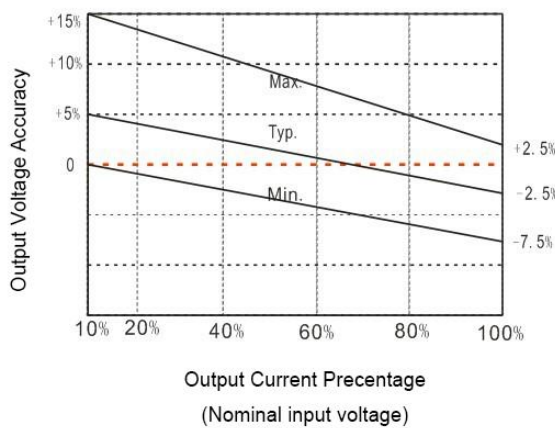
a. 12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.

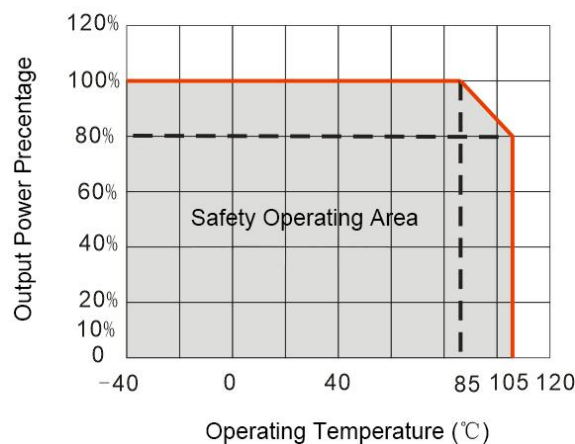
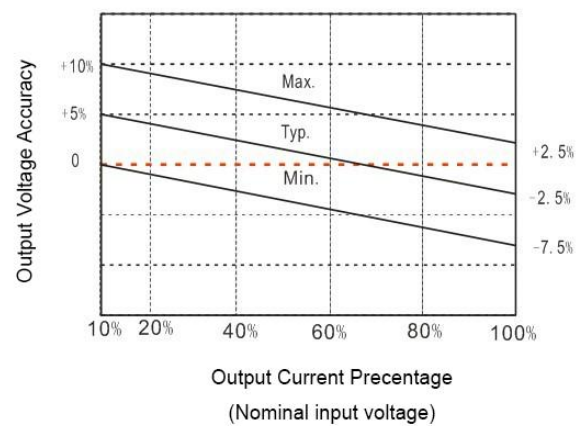


Product Characteristic Curve

3.3 VDC output
Error envelope curve



Other output
Error envelope curve



Design and Application Circuit Recommended

① Output load requirements

The maximum capacitive load of the product is obtained from the nominal full load test. When in use, it cannot exceed the maximum capacitive load of the output end, otherwise it is likely to cause startup difficulties and damage the product.

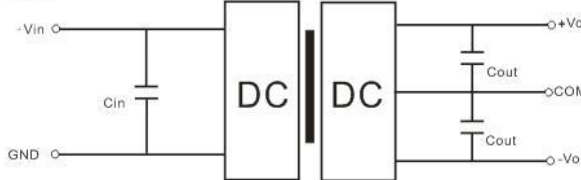
② Recommended circuit

To ensure effective reduction of input and output ripple and noise, a capacitor filter network can be connected to the input and output ends. The application circuit is shown in the figure below; but a suitable filter capacitor should be selected. If the capacitor is too large, it may affect the startup of the product. To ensure that each output works under safe and reliable conditions, the recommended capacitive load value is detailed in Table 1 below.

Single



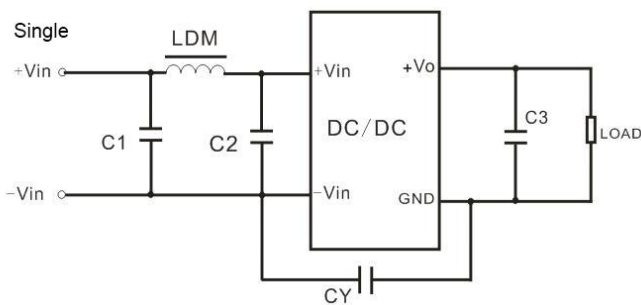
Dual



Recommended capacitive load value (Table 1)

Vin (Vdc)	Cin	Single Vout (Vdc)	Cout (μF)	Dual Vout (Vdc)	Cout (μF)
5	10 μF/16V	3.3	10 μF/16V	±3.3	4.7 μF/16V
12	2.2 μF/25V	5	10 μF/16V	±5	4.7 μF/16V
15	2.2 μF/25V	9	2.2 μF/25V	±9	2.2 μF/25V
24	1 μF/50V	12	2.2 μF/25V	±12	1 μF/25V
--	--	15	1 μF/25V	±15	1 μF/16V
--	--	24	1 μF/50V	±24	0.47 μF/50V

③ EMC Recommended Circuit



Input Voltage		3.3/5/9VDC	12/15/24VDC
EMI	C1/C2	4.7 μF/16V	4.7 μF/50V
	CY	270pF/2kV	270pF/2kV
	C3	Refer to the parameters of Cout in Table 1	
	LDM	6.8 μH	6.8 μH

④ Output voltage regulation and overvoltage protection circuit

The simplest device for output voltage regulation, overvoltage and overcurrent protection is to connect a linear voltage regulator with overheat protection in series at its input or output end and connect a capacitor filter network (see the figure below). The recommended value of the filter capacitor is detailed in (Table 1). The linear voltage regulator should be reasonably selected according to the voltage and current required for actual work; or our NW series products can be selected.

Single



Note:

1. This product cannot be used in parallel and does not support hot swapping;
2. If the product operates below the minimum required load, it cannot be guaranteed that the product performance meets all performance indicators in this manual;
3. All indicator test methods in this article are based on the company's corporate standards;
4. Product specifications are subject to change without prior notice.

Guangzhou Aipu Electron Technology Co., Ltd

Address: Building 4, HEDY Park, No.63, Punan Road, Huangpu Dist, Guangzhou, China.

Tel: 86-20-84206763 Fax: 86-20-84206762 HOTLINE: 400-889-8821

E-mail: sales@aipu-elec.com Website: <https://www.aipupower.com>